

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	1	"separate stack memory" microcontroller	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:30
S4	4294707	separate or different or other or extra	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:17
S5	614387	stack or RAM	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:17
S6	872900	memory or micro\$1processor or micro\$1controller	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:18
S7	17266	S4 near3 S5	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:19
S8	146861	S4 with S6	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:20
S9	1960	S7 same S8	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:20
S10	1851	S7 with S8	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:20
S11	90772	S4 near5 S6	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:21
S14	179333	S5 near5 S6	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:22
S16	1300	S7 same S11	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:24
S17	1194	S7 with S11	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:24
S18	908	S7 near5 S11	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:25
S19	766	S18 with S14	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:25
S22	17266	S4 near3 S5	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:26

S24	66469	S4 near3 S6	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:26
S25	603	S22 near3 S24	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:31
S26	164386	S5 near3 S6	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/07 09:31
S27	103	S25 near S26	US-PGPUB; USPAT; USOCR	AND	OFF	2005/10/12 09:36

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2309147	partition\$3 or different or distinct or detach\$2 or divide\$1	USPAT	OR	OFF	2005/10/14 14:52
S2	157530	stack	USPAT	OR	OFF	2005/10/14 14:53
S3	120050	S1 and S2	USPAT	OR	OFF	2005/10/14 14:55
S4	13597	S1 with S2	USPAT	OR	OFF	2005/10/14 14:56
S5	88533	S1 with memory	USPAT	OR	OFF	2005/10/14 14:56
S6	555	S5 with S2	USPAT	OR	OFF	2005/10/14 15:00
S10	125	S6 and "711".clas.	USPAT	OR	OFF	2005/10/14 15:00

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	(microprocessor or processor) with "8-bit instruction set"	USPAT	OR	OFF	2005/10/17 14:40
S2	393	(microprocessor or processor) with ("8-bit" and "instruction set")	USPAT	OR	OFF	2005/10/17 14:42
S3	130	microprocessor with ("8-bit" and "instruction set")	USPAT	OR	OFF	2005/10/17 15:17
S4	5	microprocessor with ("8-bit" near3 "instruction set")	USPAT	OR	OFF	2005/10/17 15:17

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5	"256 byte" near "data memory"	USPAT	OR	OFF	2005/10/17 16:53
S2	66385	"data memory"	USPAT	OR	OFF	2005/10/17 16:53
S3	1341	"stack memory"	USPAT	OR	OFF	2005/10/17 16:53
S4	238512	"256"	USPAT	OR	OFF	2005/10/17 16:53
S5	81984	byte\$1	USPAT	OR	OFF	2005/10/17 16:54
S6	3953	S4 adj S5	USPAT	OR	OFF	2005/10/17 16:54
S7	12	S6 near S2	USPAT	OR	OFF	2005/10/17 16:54
S8	1	S6 near S3	USPAT	OR	OFF	2005/10/17 16:54
S9	13	S6 near (S2 or S3)	USPAT	OR	OFF	2005/10/17 17:00
S10	44	S6 near3 (S2 or S3)	USPAT	OR	OFF	2005/10/17 17:17
S11	502129	S6 near S3 memory	USPAT	OR	OFF	2005/10/17 17:18
S12	18114	S11 and "711".clas.	USPAT	OR	OFF	2005/10/17 17:18
S13	11199	S12 and @py<"2002"	USPAT	OR	OFF	2005/10/17 17:20
S14	648	S6 near3 memory	USPAT	OR	OFF	2005/10/17 17:20
S15	101	S14 and "711".clas.	USPAT	OR	OFF	2005/10/17 17:22
S16	54	S15 and micro\$1processor	USPAT	OR	OFF	2005/10/17 17:22